1. Introduction

In fiber optic communications we can see a continuing growth in transmission capacity and speed basically due to advances in development of optical components and systems in a physical layer (new components are still needed for optical signal processing), for example laser diodes - DFB-lasers, DBR-lasers, Mode-locking lasers...; new hybrid optical amplifiers - Raman/EDFA; and so on. Further, we can see that development in optical components leads up to miniaturization and integration and, therefore, in the future we will prefer those devices which can satisfy this condition while remaining cost competitive. For future generation of optical networks to utilize the full bandwidth of optical fiber we expect such systems (Dense Wavelength Division Multiplexed - DWDM), which will have real bit-rate of individual channels out of range of electronics components. Now practically whole signal processing with optical stream is performed in electrical domain or rarely in hybrid devices - Fig. 1.

In such devices we have to convert an input optical signal to electrical domain where we can perform required processing and then convert it back to an optical signal. The odds are that such devices have their disadvantages (this double conversion is time-consuming and expensive). Hence we want to overcome or reduce O-E-O conversion. Illustration of such processing can be seen in figure 2.
In the present time, the developers produce many architectures for all-optical signal processing (but not yet in optimized fabricable application) and SOAs with long recovery times (> 100 ps) seem to demonstrate efficient interferometric all-optical switches to deliver switching and demultiplexing on Tbps data-rate streams. These nonlinearities are typically based on resonance excitation in an actively biased optical amplifier or passive semiconductor nonlinear waveguides. Extensive experimental [1–6] and theoretical [6] analyses have been performed on various configurations of these devices.

2. SOA architectures

In this section, we review some of the most promising applications of ultra-fast, SOA-based optical switches under investigation at the University of Zilina. We introduce the architectures for all-optical switches and characterize their performance.

The main SOA-based architectures are (figure 3): TOAD – Terahertz Optical Asymmetric Demultiplexer, CPMZ – Colliding Pulse Mach-Zehnder and SMZ – Symmetric Mach-Zehnder. The TOAD is based on Sagnac interferometer. It was discovered that the temporal width of the switching window could be linearly controlled by adjusting the displacement of the SOA from the midpoint of the loop. Due to dynamics of this configuration, the switching window actually closes earlier than the recovery time of the SOA as the SOA is moved closer to the midpoint. In the absence of a control pulse, data pulses enter the fiber loop, pass through the SOA at different times as they counter-propagate around the loop and recombine interferometrically at the coupler (data is reflected due to same changes in SOA). In the presence of control pulse switching can occur, because control pulse saturate SOA and change its index of refraction, therefore we can achieve different phase shift between two counter-propagating data pulses. The temporal duration of the switching window is determined by the offset of the SOA, \( \Delta \lambda_{SOA} \), from the center position of the loop.

\[
\tau_{min} = \frac{2\Delta \lambda_{SOA}}{c_{fiber}}
\]

where \( c_{fiber} \) is speed of light in fiber.

Using a similar operating principle it is possible to form other interferometric configuration based on SOA. These architectures improve the integerability and performance of the device, although they require active stabilization if constructed from discrete components. Two variations of the switch in Mach-Zehnder interferometer (MZI) configuration are shown in figure 3. In the absence of the control signals, the MZI is balanced so that the data signals are rejected from the output port. When control pulses are injected into the interferometer, a differential phase shift is briefly introduced between the two arms of the interferometer causing the data pulse to be switched to the output port. The advantage of the Colliding Pulse MZI (CPMZI) is requirement of no output filter. The nominal switching window of CPMZ and SMZ are:

\[
\tau_{min} = \frac{2\Delta \lambda_{SOA}}{c_{fiber}} \quad \tau_{min} = \Delta t_{cs} \text{ respectively.}
\]

Fig. 3. The main SOA-based architectures: TOAD, CPMZ and SMZ

Fig. 4. Optical switching window width simulation results. a) TOAD, b) CPMZ and c) SMZ architecture
3. Simulation of the switches

While many theoretical models have been developed to understand the ultrafast temporal response of SOAs [7–9] we present a summary of the simulated performance of the three geometries introduced in the previous section. The switching windows are acquired by using a scanning pump-probe apparatus described in [6].

The switching window provides information regarding the shape, amplitude, and temporal width of the optical transfer function. This characterization is useful for determining the optical demultiplexing and sampling bandwidth of the switch. For the results described here, various pulse energies, width, and repetition rates were used to demonstrate the effect, where these parameters affect the switching performance.

The shortest switching window achieved by TOAD was about 3.8 ps (FWHM), by CPMZ about 8 ps and by SMZ even 1.5 ps. This performance indicates that the SMZ structure may be suitable for demultiplexing from 660 Gbps data stream. Reducing the optical switching window further to subpicosecond regimes may be difficult due to SOA gain compression. Further investigation and measurements of these dynamics are important for future research.

4. Summary

In this paper we presented new architectures as TOAD, CPMZ and SMZ interferometrical switches. We briefly introduced their performance and operating principle. Using mathematical simulation in VPIprofessionalTool® software we investigated their parameters such as optical temporal window width. We demonstrated that SMZ architecture is ultrafast operating switch able to demultiplex 660 Gbps data stream.

References:


