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VOLTAGE STRESS REDUCTION ON COMPENSATION CAPACITORS OF WIRELESS CHARGING SYSTEMS FOR TRANSPORT AND INDUSTRIAL INFRASTRUCTURE

The paper deals with a proposal for circuit configuration of series-series (SS) compensated wireless power transfer (WPT) system with reduced voltage stresses on compensation capacitors. Nowadays, a huge effort is given for the development of reliable, efficient and robust wireless charging systems and therefore the circuit configuration and component selection are crucial to meet suitable operational characteristics. For this purpose, the current trends in WPT systems are outlined in the first section. Next, the analysis of frequently utilized WPT system configuration is provided and the most critical issues related to practical utilization are discussed. Consequently, the proposal of how to eliminate negative aspects is given. All theoretical statements are supported by mathematical apparatus of the proposed system and consequently by the simulation analysis.

Keywords: wireless power transfer, resonance, capacitor, voltage stress

1. Introduction

Development of power electronics in last decades enables the massive rise of consumer and industrial electronic devices. Together with this rise, the autonomy of electronics becomes also very important. This means that self-operation without the necessity of component supplement with grid electricity acts as a major feature how to become independent. The way of such realization is the utilization of energy storage systems like accumulators or super capacitors. Nowadays, almost in every application, the usage of long-durability accumulators is necessarily inevitable (cell-phones, laptops, lighting, biomedical, etc.) [1-3]. Current trends in energy recharge are still mostly based on the wired chargers, but the wireless power transfer (WPT) is increasingly getting into the forefront of technology when talking about advanced charging systems [4-5].

IPT (inductive power transfer), as well as CPT (capacitive power transfer), are the most pervasive methods used for wireless charging [6-8]. IPT is most common and is applicable to many power levels and gap distances. CPT is only applicable for power transfer with inherently small distances. There are also various requirements for main circuit configuration e.g. configuration of resonant compensation network etc. This affects the system transfer characteristics and electrical behavior of key circuit components [9-10]. It is now well known that series-series compensation is the most suitable for the wide spectrum of practical applications. However, one disadvantage is typical and must be carefully considered, it is voltage stress of compensation capacitors. This voltage stress is several times higher (it depends on the operational quality factor) against the supply voltage. For that reason, the structural design of optimal compensation capacitor is quite a difficult issue. A possible way is to use either a special high-voltage capacitor which is very expensive or series-

parallel connection of several capacitors even with high-voltage sustainability. This can be a limiting factor for many applications especially when the cost comes into account.

This paper proposes a solution for SS compensation system with reduced voltage stress of compensation capacitors. Given recommendations are initially expressed by the mathematical apparatus, while dependencies of critical variables (compensation capacitor voltage) are identified. The principal operation of system proposal is then verified by the simulation model with high level of validity.

2. Series-series resonant compensated WPT system

The equivalent circuit of the WPT system with resonant compensation configured in series-series topology is shown in Figure 1. The compensation capacitors C_1 and C_2 are connected to the primary and the secondary coil in series. Based on this, the resonant tank acts as a band-pass filter with relatively good selectivity.

It is generally known that to maximize the output power, both parasitic resistances (R_1 and R_2) must be minimized as much as possible [11]. To evaluate the behavior of the system, the basic circuit analysis was performed. The simulation parameters are taken from two identical experimental prototypes of coupling coils. Their values are therefore the same on the primary and the secondary side. The principal design procedure has been already described in [12-13], therefore it won't be discussed here more in detail. The self-inductances are equal to $L_1=L_2=127.4 \mu\text{H}$, parasitic resistances are $R_1=R_2=0.447 \Omega$ and the load is $R_L=30 \Omega$. The operational frequency range is set according to the main resonant frequency ($f_{\text{range}}=290 \text{ kHz} \div 340 \text{ kHz}$). Both compensation capacitors have also the same capacity $C_1=C_2=2 \text{ nF}$.

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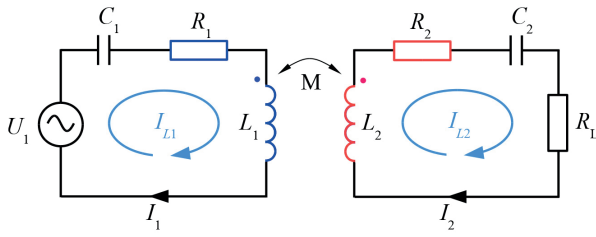


Figure 1 The equivalent circuit of WPT with series-series resonant compensation

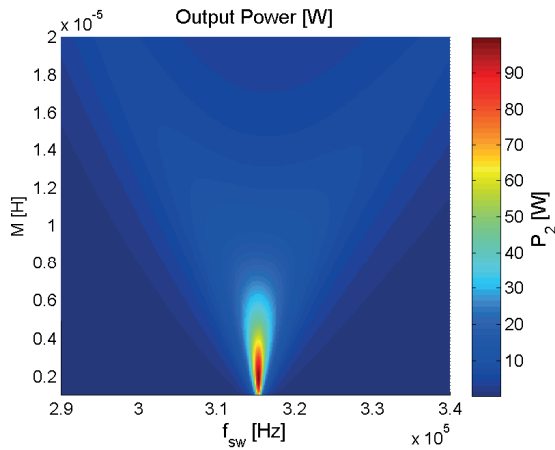


Figure 2 Power delivered to the load in case of resonant coupling with series-series compensation

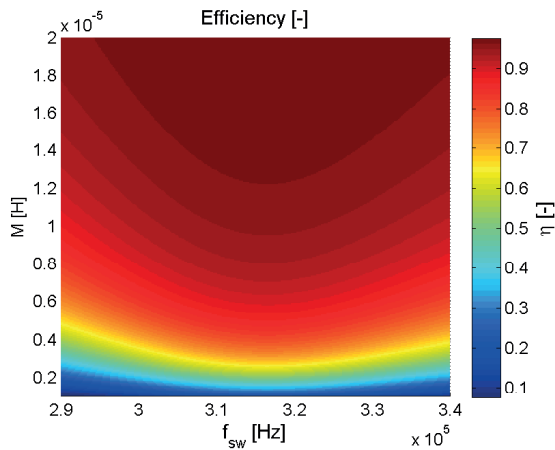


Figure 3 Transmitting efficiency in case of resonant coupling with series-series compensation

In the simulation, the minimal mutual inductance $M_{min} = 1 \mu\text{H}$ corresponds to the transmitting distance 55 cm while its maximal value $M_{max} = 20 \mu\text{H}$ represents the distance 10 cm. Simulated distance range was chosen according to requirements for battery charging of electrical vehicles, i.e. for the transportation systems.

Power delivered to the load and the resulting efficiency is seen in Figures 2 and 3. The figures are constructed (using MATLAB) as follows. The operating (switching) frequency is given in x-axis while the y-axis represents the various mutual inductance. It is

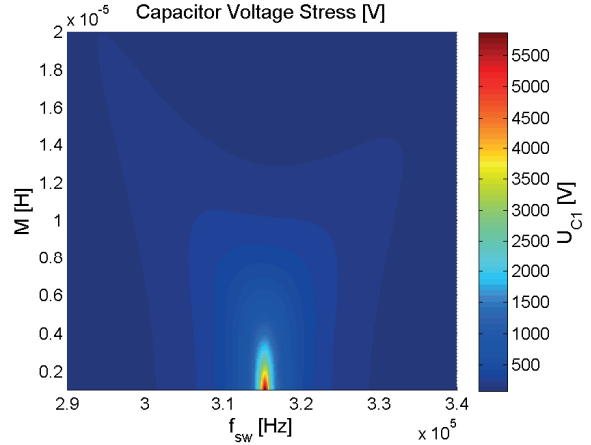


Figure 4 The voltage on the compensation capacitor C_1 for standard SS WPT system

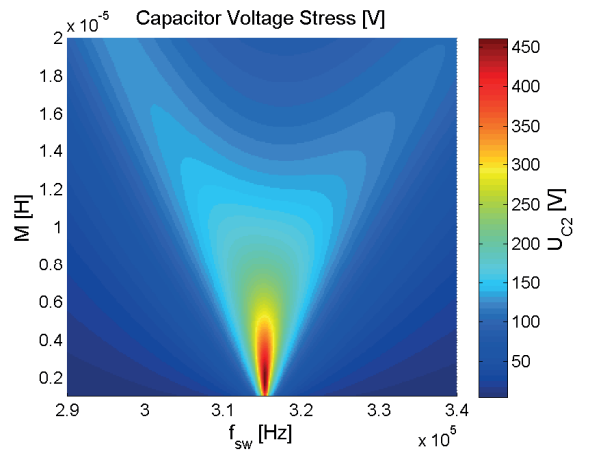


Figure 5 The voltage on the compensation capacitor C_2 for standard SS WPT system

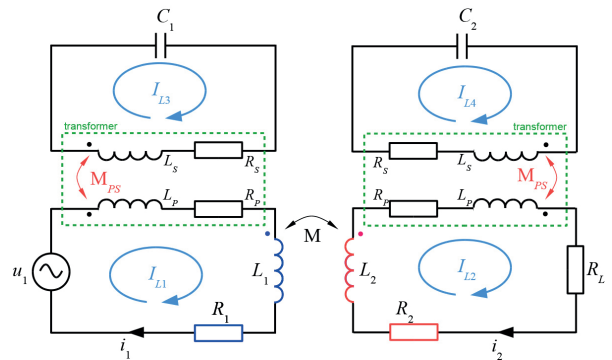


Figure 6 The equivalent circuit of WPT with series-series resonant compensation and reduced voltage stress on compensation capacitors

therefore, a 3D interpretation of particular quantities expressed as a function of two variables.

Based on this, it is clear that the operation of the system can be ensured under various conditions. It depends on the target application and actual system conditions whether high power and mid-range efficiency or very high efficiency and mid-range power are required. Mentioned parameters are strictly dependent on the optimal load (value of R_L) of the WPT system, which is for individual cases different and must be therefore exactly identified [13].

$$\begin{bmatrix} \hat{U}_1 \\ 0 \\ 0 \\ 0 \end{bmatrix} = \begin{bmatrix} \left\{ \begin{matrix} R_1 + R_P + \\ j\omega(L_1 + L_P) \end{matrix} \right\} & -j\omega M & -j\omega M_{PS} & 0 \\ -j\omega M & \left\{ \begin{matrix} R_P + R_2 + R_L \\ j\omega(L_1 + L_P) \end{matrix} \right\} & 0 & -j\omega M_{PS} \\ -j\omega M_{PS} & 0 & \left\{ \begin{matrix} R_S - \\ j\frac{1}{\omega C_1} + j\omega L_S \end{matrix} \right\} & 0 \\ 0 & -j\omega M_{PS} & 0 & \left\{ \begin{matrix} R_S - \\ j\frac{1}{\omega C_2} + j\omega L_S \end{matrix} \right\} \end{bmatrix} x \begin{bmatrix} \hat{I}_{L1} \\ \hat{I}_{L2} \\ \hat{I}_{L3} \\ \hat{I}_{L4} \end{bmatrix} \quad (1)$$

$$f_o = \frac{1}{2\pi\sqrt{L_S C_{1,2}}}, L_S = \frac{L_P}{n^2} \quad (2)$$

$$U_{C1} = \left| \frac{\left[R_2 + R_L + j\left(\omega L_2 - \frac{1}{\omega C_2} \right) \right] \hat{U}_1}{\left[R_1 + j\left(\omega L_1 - \frac{1}{\omega C_1} \right) \right] \cdot \left[R_2 + R_L + j\left(\omega L_2 - \frac{1}{\omega C_2} \right) \right] + (\omega M)^2} \right| \cdot \frac{1}{n\omega C_1}, \quad (3)$$

$$U_{C2} = \left| \frac{j\hat{U}_1 \omega M}{\left[R_1 + j\left(\omega L_1 - \frac{1}{\omega C_1} \right) \right] \cdot \left[R_2 + R_L + j\left(\omega L_2 - \frac{1}{\omega C_2} \right) \right] + (\omega M)^2} \right| \cdot \frac{1}{n\omega C_2}.$$

Figures 4 and 5 show the voltage stress of particular compensation capacitors (C_1 , C_2). It can be seen that even for the very low value of supply voltage (in this case 30 V), the peak voltage at each component is several times higher than the supply voltage. The situation is most critical when the system operates at a resonant frequency, or at its close range and also when the low value of mutual inductance is formed by high operational distance. Carefully designed capacitor structure and its physical configuration is therefore the most difficult issue [14-17].

3. WPT system with reduced voltage stresses on compensation capacitors

As previously mentioned, most limiting factor for practical use of S-S compensated WPT system is high voltage stress on compensation capacitors. A possible way how to solve this problem is to use either an appropriate series-parallel connection of discrete capacitors with the required value of capacitance and voltage sustain level or special high voltage capacitors which are usually very expensive [18-19]. Another possibility is to lower the coils' inductance which reduces the overall stress voltage across the resonant tank. This solution may debase the value of the coil's quality factor and consequently shorten the usable distance range.

Figure 6 shows proposed resourceful circuit modification which significantly reduces voltage stress on compensation capacitors. The solution uses ordinary components and is therefore cheaper. The system may be sufficiently described by the loop current method as seen in Equation (1). It is considered that the variables marked with ^ are considered as peak values.

Both compensation capacitors are replaced with a combination of capacitors and transformers whose secondary circuits are tuned at the resonant frequency given in Equation (2). The system behavior is quite sensitive to the selection of primary inductance L_P and transformer voltage ratio n . Its leakage inductance must be minimized as much as possible.

With the simplification of (1) in the way the transformers are considered as ideal with voltage ratio n , we may derive a general equation for voltages on compensating capacitors (3) in dependency on the frequency.

4. Frequency analysis of proposed system

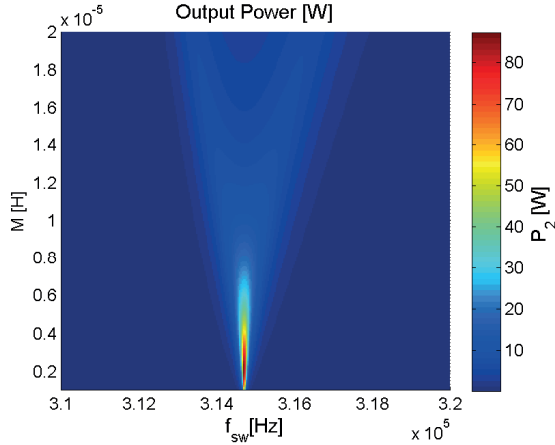
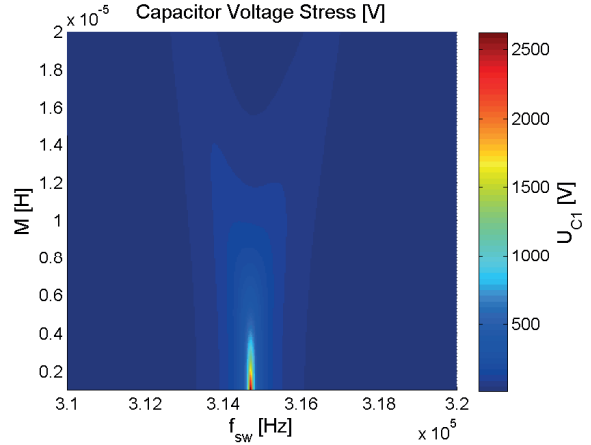
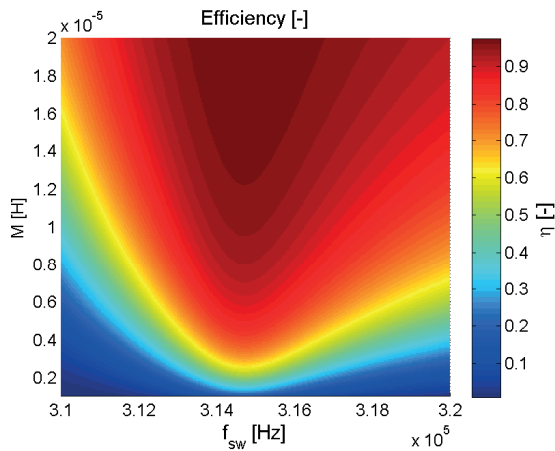
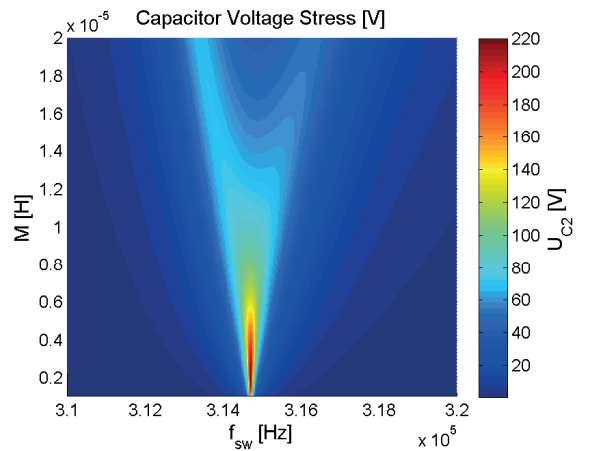
In order to validate the proposed circuit solution, the system analyses have been done using Equation (1). The output power, transmitting efficiency and voltage stress on C_1 and C_2 corresponds to a voltage ratio of compensation transformer $n=2$.

The simulation parameters are same as used in the previous model and thus the self-inductance equals to $L_1=L_2=127.4 \mu\text{H}$, parasitic resistance is $R_1=R_2=0.6 \Omega$ and the load is $R_L=30 \Omega$. The analyzed frequency range $f_{\text{range}}=310 \text{ kHz} \div 320 \text{ kHz}$ is not as wide as found in the previous case. Primary inductance of both compensation transformer on the transmitting and receiving side is set to $L_P=10 \mu\text{H}$. Secondary inductance is calculated from Equation (3), $L_S=2.5 \mu\text{H}$. The parasitic resistances on the secondary side of the compensation transformer plays an important role and was therefore minimized $R_S=0.025 \text{ m}\Omega$. Primary resistances are considered within resistances R_1 and R_2 . Finally, compensation capacitors C_1 and C_2 were determined from Equation (4) and $C_1=C_2=110 \text{ nH}$. Minimal mutual inductance is $M_{\text{min}}=1 \mu\text{H}$ while its maximal value $M_{\text{max}}=20 \mu\text{H}$.

Simulation results for the low value of transformer ratio are shown on Figures 7-10. As mentioned before, frequency operational range became narrower which is obvious from all discussed results.

Principal aim for our modified S-S system is to reduce voltage stress on compensation capacitors. Compared to the standard solution the voltage (Figures 4 and 5) on the compensation capacitors is due to compensation transformer reduced up to 3-times (Figures 9 and 10) for individual operating regions.

On the contrary, it can be seen (Figure 8) that even low value of parasitic resistances measured on the secondary side

Figure 7 Power delivered to the load ($n=2$)Figure 9 The voltage on the transformed ($n=2$) compensation capacitor C_1 Figure 8 Transmitting efficiency ($n=2$)Figure 10 The voltage on the transformed ($n=2$) compensation capacitor C_2

of compensation transformers greatly influences transmitting efficiency. Similarly, to other variables, the frequency range became narrower whereby area with high efficiency (above 90%) became smaller as compared to Figure 3.

The previously described analysis of the proposed system behavior was further verified by the circuit simulation in time-domain, whose results are shown within the next chapter.

5. Simulation analysis of proposed system

The proposed circuit solution was verified also with the use of transient simulation (Figures 14 and 15) implemented as PSpice model. Figure 11 shows the full circuit configuration which is suitable for charging or supplying various devices in this case for demonstration lap-tops, tablets or LCD displays, but it is not explicitly excluded that the proposed methodology is suited just for this kind of application. All key circuit components are set according to the previous model described in section IV. i.e.:

- $U_i = 30$ V
- $L_1 = L_2 = 127.4$ μ H
- $R_1 = R_2 = 0.6$ Ω
- $R_L = 30$ Ω
- $f_{\text{range}} = 310$ kHz \div 320 kHz
- $L_p = 10$ μ H.
- $L_s = 2.5$ μ H.
- $R_s = 0.025$ m Ω .
- $C_1 = C_2 = 110$ nH.
- $M_{\min} = 1$ μ H
- $M_{\max} = 20$ μ H.

Only two mutual positions (mid-distance and maximal distance) of transmitting and receiving coils are simulated. The minimal considered distance is 25 centimeters which corresponds to the coupling coefficient between respective coils $k=0.09$ ($M=11$ μ H). The maximal distance investigated is 55 cm which changes the coupling coefficient in $k=0.04$ ($M=5$ μ H).

Investigation of the transfer properties of standard SS resonant WPT system and WPT system with compensation transformer were realized within frequency sweep analyses. It is obvious that for selected mutual positions the system reaches the maximal available power and efficiency precisely at the resonant point. This operating state is optimal for the transmitting power and the efficiency but in case of standard circuit configuration (Figure 1) it is highly stressful for the resonant capacitors (Figure 12), where maximum of the voltage stress is achieved for the resonant point of operation [10]. Similar situation but for the system with compensation transformer is shown in Figure 13. The maximal power delivery and efficiency are achieved again close to the resonant point, and capacitor voltage stress is at its peak, but compared to results from Figure 12, it is half of its value. It must be mentioned here, that the resonant points are different when standard and optimized WPT systems are considered. This is caused by the slightly different capacitor value when compensation transformer is considered, while its value is

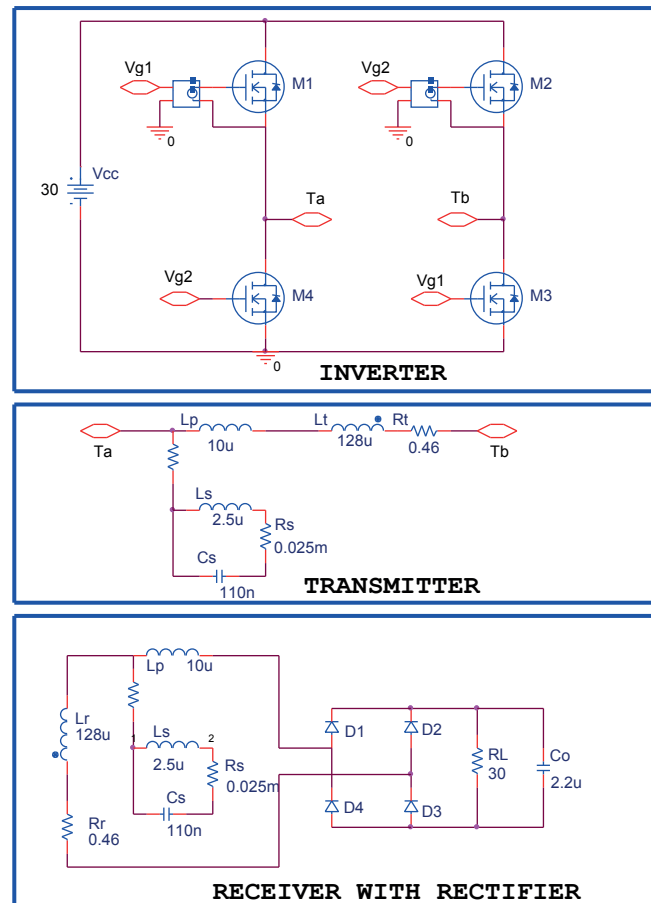


Figure 11 A simulation model of the proposed WPT system voltage on the transformed ($n=2$) compensation capacitor $C1$

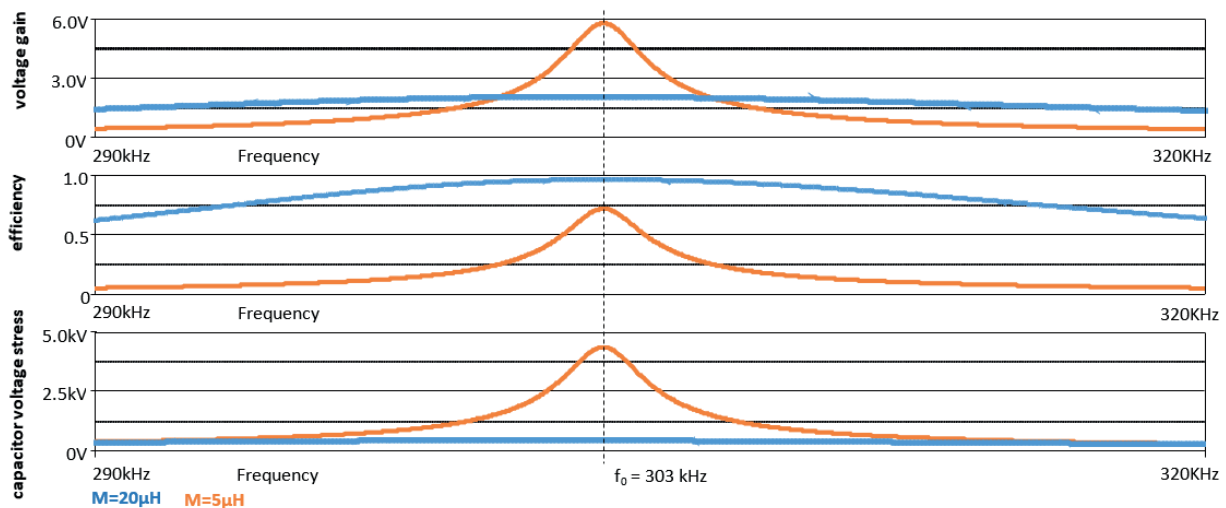


Figure 12 Transfer characteristic for standard SS resonant compensated WPT system (resonant point = 303 kHz)

dependent on the integer value of the number of compensation transformer turns.

On the other hand, the achievable power transfer was decreased from 110 W to 55W and efficiency from 80% to 65%. This was expected because of the existence of parasitic components of compensation transformer (parasitic resistances of windings, leakage inductance). Those must be decreased as much as possible that will be the main topic for the future works,

which will be related to optimization works on the compensation transformers as well as on the experimental verifications of the system proposal.

Consequent simulation experiments have been done within time-domain (Figures 14 and 15), while the mutual distance between the transmitting and receiving side was considered for the situation, where $M = 5 \mu\text{H}$ (distance = 55 cm). In comparison to the proposed compensation technique the voltage stress of

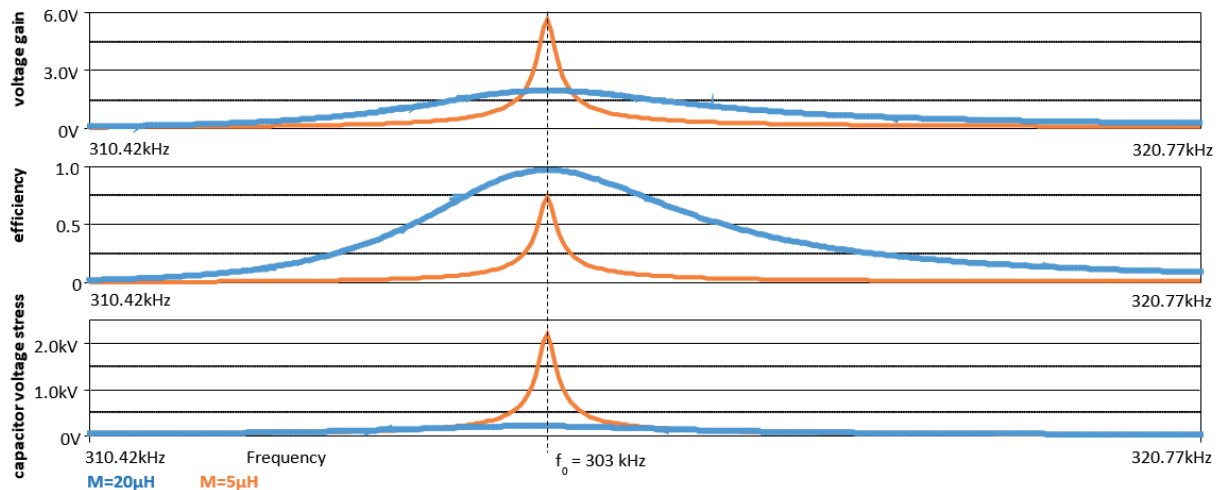


Figure 13 Transfer characteristic for WPT system with compensation transformer (resonant point = 315 kHz)

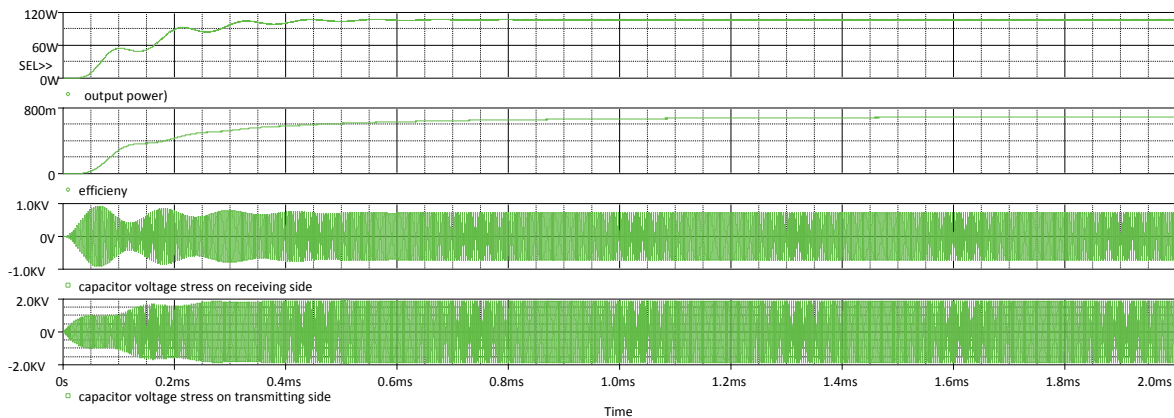


Figure 14 Simulation results of WPT system without compensation transformer (standard SS compensated system) operated at the the resonant point

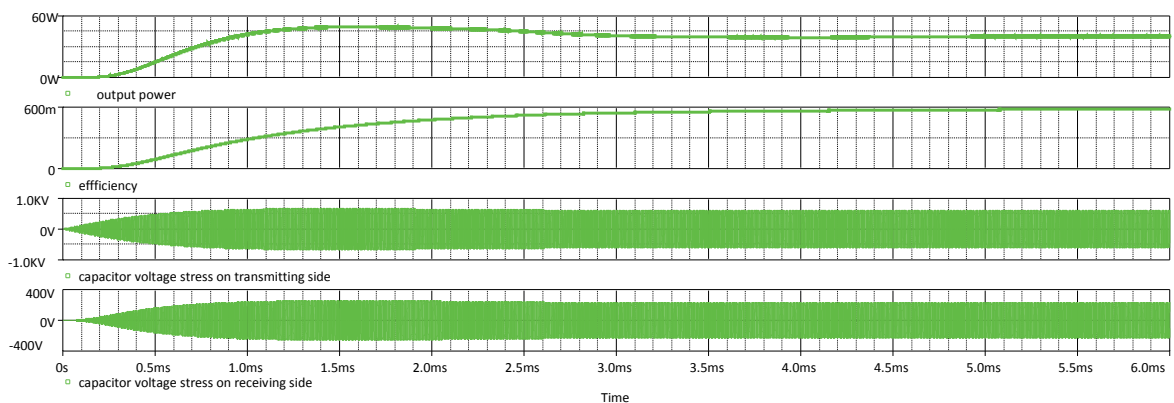


Figure 15 Simulation results of WPT system with compensation transformer operated at the resonant point

capacitors in the case of standard SS compensated WPT system is almost 4 times higher. Capacitor voltage stress for standard solution is around 2 kV for transmitting side, while for receiving side it is around 750 V (Figure 14). Proposed solution is acting as follows; transmitting side has voltage stress at the level of 600 V, while receiving side around 200 V (Figure 15).

6. Conclusion

In this paper, a new compensation technique for the resonant WPT system is presented. The simple design procedure is given, whereby the negative effect of individual circuit components is identified. The proposed solution significantly reduces the voltage stress of compensation capacitors. This is basically dependent on the design of the compensation transformer. Within proposed

system, reduction of the voltage stress was achieved up to 3-times lower compared to standard WPT system. Simulation clearly shows all fundamental advantages and disadvantages for further practical use. It was found that the proper design of the compensation transformer will be important issue for future works, thus it has the most significant impact on the whole system behavior while thinking about system efficiency and power delivering capability. Practically all investigated variables are influenced by the values of parasitic resistances on the transformer secondary side where high circulating current occurs. In other words, the higher transformer ratio enables to reduce voltage stress markedly, but on contrary it increases circulating secondary currents. Therefore, it is necessary to find out a suitable compromise that will meet all design requirements.

Future work will be focused on the practical verification of proposed solution and on its system optimization.

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